

In Place of FORM PTO-1449 (Modified)

Serial No. 10/626192**LIST OF PATENTS AND PUBLICATIONS FOR  
APPLICANT'S INFORMATION DISCLOSURE  
STATEMENT**Applicant: Accapadi, et. al.Filing Date: 7/24/2003Group: 2109Atty. Docket No. AUS920030231US1**U. S. PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
/MZ/	AA 2001/0056456	Dec. 27, 2001	Cota-Robles	709	103	
	AB 2002/0124042	Sep. 5, 2002	Melamed et al.	709	102	
	AC 2002/0078119	Jun. 20, 2002	Brenner et al.	709	102	
	AD 6,212,544	Apr. 3, 2001	Borkenhagen et al.	709	103	
	AE 6,108,683	Aug. 22, 2000	Kamada et al.	709	103	
	AF 5,937,187	Aug. 10, 1999	Kosche et al.	395	674	
	AG 5,752,031	May 12, 1998	Cutler et al.	395	673	
	AH 5,428,789	Jun. 27, 1995	Waldron, III	395	700	
	AI 2002/0184290	Dec. 5, 2002	Olszewski et al.	709	102	
	AJ 2002/0178208	Nov. 28, 2002	Hutchison et al.	709	102	
	AK 2002/0129227	Sep. 12, 2002	Arakawa	712	228	
	AL 6,263,359	Jul. 17, 2001	Fong et al.	709	103	
	AM 5,946,466	Aug. 31, 1999	Adiga et al.	395	200.61	
	AN 5,630,128	May 13, 1997	Farrell et al.	395	673	
	AO 5,515,538	May 7, 1996	Kleiman	395	733	
	AP 5,247,675	Sep. 21, 1993	Farrell et al.	395	650	
/MZ/	AQ 5,220,653	Jun. 15, 1993	Miro	395	275	

**FOREIGN PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
/MZ/	AR JPI1327921	Nov. 30, 1999	Japan	G06F9	46	N

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
/MZ/	AS	Fiske et al., "Thread Prioritization: a thread scheduling mechanism for multiple-context parallel processors," High-Performance Computer Architecture, Jan 1995, p. 210-221.				
/MZ/	AT	"Deterministic Priority Inversion Method for Personal Computers," IBM Technical Disclosure Bulletin, May 1995, Vol. 38, No. 05, p. 271-276.				
/MZ/	AU	"Providing an Application with Limited Control of Preemption," IBM Technical Disclosure Bulletin, Sep. 1995, Vol. 38, No. 09, p. 187-188.				
/MZ/	AV	"Lock priority boosting in an MP system with multiple run queues," IBM Research Disclosure, Jan. 2001, p. 174.				

Examiner: /Meng Yao Zhe/

Date Considered: 03/28/2007

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.